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INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

To Commissioner For Patents Enclosed herewith is a Form PTO-1449, any required copies of documents listed thereon, and any concise explanation of their relevance is indicated below per 37 CFR 1.97.

Application Number	
Filing Date	
First Named Inventor	DE CLERCQ, MARK N., O.
Group Art Unit	
Examiner Name	
Atty. Docket Number	NL04 0156 US1

			U.S. P	ATENT DOCUMENTS				
Examiner Initials*	Cite No.	Document Number NoKind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Docum		Pages, Columns Lines, Where Relevar Passages or Relevant Figures Appear		
/L.C./	1	us- 2003/132777	07/17/2003	LAACKMANN PETER ET	AL.			
/L.C./	2	us- 2003/218475	11/27/2003	GAMMEL BERNDT				
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		US-						
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		US-						
FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.1	Document Number (ctry3-no.4-kind5, if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of cited docume	l v	Pages, Columns Lines, Mere Relevant Passages Relevant Figures Appear	₩,	
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	gr.	N	ON-PATENT	LITERATURE DOCUME	NTS	\$4,44.00 mg	183	
Examiner Initials*	Cite No.1	include name of the author (in capital letters), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.						
/L.C./	1	I. RAYANE ET AL: "A DIGITAL BIST FOR OPERATIONAL AMPLIFIERS EMBEDDED IN MIXED- SIGNAL CIRCUITS"; PROCEEDINGS OF IEEE VLSI TEST SYMPOSIUM: 1999 PP 304-310.						
/L.C./	2	KHALKHAL A ET AL: "ON-CHIP MEASUREMENT OF INTERCONNECT CAPACITANCES IN A CMOS PROCESS", PROCEEDINGS OF THE INTERNATIONAL CONF. ON MICROELECTRONIC TEST STRUCTURES NARA JP; MAR 22-25 1995; N.Y. IEEE US; 22 MAR 1995; PP 145-149						
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Examine Signatur		/Long	pbit Chai/		Date Considered	10/16/2008		

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